# Minutes

Meeting: PESC Technical Advisory Board Meeting

Time: Thursday, September 23, 2010, 3 PM EDT

Call-in Number: 1-866-352-3799 code: \*7696363\*

# Attendees:

* Russ Buyse
* Jeff Funck
* Steve Margenau
* Michael Morris

# Agenda

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| --- | --- | --- |
| **Topic** | **Lead** | **Actual Time** |
| Action Item Review | Michael | 0 |
| Component Architecture | Michael | 75 |
| Meeting Summary | Michael | 0 |

Minutes: Morris

# Old Action Items

* Complete: Michael M. will put a change log for the Architecture document on the AirSet TAB Site using an Excel Workbook. The first change item will be that the same type name may not be used if that type name is already in core-main.
* Complete: Steve will put a copy of the PESC Guidelines for Architecture and Data Modeling on Airset so that we can edit different sections independently.
* Complete: Jeff will inform the CCB that we will be cleaning up some of the sector libraries and top level schemas to eliminate reusing names for extended types.
* Complete: A volunteer is needed to see how we can add attributes to named complex type, simple type, group definitions, and global elements so that we can indicate the source of the component (core, sector, etc.) and its version.

# Decisions:

* Remove comments derived from sector library and core main during the single file schema transform process
* Add step to current schema development process so that the single file schema is produced after components have been put in core main by CCB. This needs to be completed before public comment.

# New Action Items:

* Michael will investigate XML Spy to see if it can integrate with third party services like the Registry and Repository to retrieve components.

# Discussion:

* Jeff suggested that we need a schema development environment like XML Spy that could pull components from the R& R and drop them into a schema. Either an open source tool could be modified to access R&R web service or a commercial tool could be found that could interface either directly or through some middleware code.